# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named

Inventor : Garrie D. Huisenga

Appln. No.: 10/825,475

Filed : April 15, 2004

Group Art Unit: 2836

For : PROCESS DEVICE WITH

SWITCHING POWER SUPPLY

Docket No.: R11.12-0856

Examiner:

# INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

I HEREBY CERTIFY THAT THIS PAPER IS BEING SENT BY U.S. MAIL, FIRST CLASS, TO THE COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, THIS

J DAY OF

2004

Sir:

The patents or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 C.F.R. § 1.97. Copies of "other art" cited are enclosed.

### TIME OF FILING

The information disclosure statement is being filed:

- 1. X with the application or within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. In accordance with 37 C.F.R. § 1.97(b), no statement or fee is required.
- 2. \_\_\_\_ after the time period specified in paragraph 1 above, but before the mailing date of a final action under 37 C.F.R. § 1.113 or notice of allowance under 37 C.F.R. § 1.311. Therefore, in accordance with 37 C.F.R. § 1.97(c), submitted herewith is:

(check either A or B below)

A. a statement as specified in 37 C.F.R. § 1.97(e).

- B. \_\_\_ the fee set forth in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c).
- 3. \_\_\_ after the mailing date of either a final action under 37 C.F.R. § 1.113 or a notice of allowance under 37 C.F.R. § 1.311, whichever occurs first, but before payment of the issue fee. Therefore, Applicant petitions for consideration and submits herewith:
  - A. a statement as specified in 37 C.F.R. § 1.97(e);
  - B. the petition fee set forth in 37 C.F.R. § 1.17(p).

#### STATEMENT

(only used if No. 2(A) or No. 3 above is checked) The person(s) signing below certify  $\dot{}$ 

(check appropriate paragraph)

that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. 37 C.F.R. § 1.97(e)(1).

OR

that no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement. 37 C.F.R. § 1.97(e)(2).

# METHOD OF PAYMENT

X	_ No fe	ee	is	required.							
	Attached	is	а	check	in	the	amount	of	\$		

The Director is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 23-1123. A duplicate copy of this communication is enclosed.

Respectfully submitted,

WESTMAN, CHAMPLIN & KELLY, P.A.

BV

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JKC/rkp

FORM PTO-1449										Docket	Appl. No.: 10/825,475			
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT								First Named Inventor:						
								Garrie D. Huisenga						
									ling	Date	Group Art:			
								Apı	ril	15, 20	2836			
				,	J.S. PA	ATENT D	OCUMENTS							
		Document	B		N			Class		Sub	Filing Date			
Initia		No.	Date		Name			Class		Class	If Appropriate			
	AA	4,206,297	06/1980		Dahlke				323		22			
<del></del>	AB	4,502,318	03/1985		Converse III et al.					73	3			
	AC	4,621,551	11/1986		Silverman				<u> </u>	82	31			
	AD	4,755,773	07/19		Ohmagari				332		9			
F	AE	4,804,958	02/19		Longsdorf				340		860			
F	AF	4,833,922	05/19	89	Frick et al.				73		756			
F	AG .	4,866,435	09/19	89	Frick				340		870.16			
I	HA	4,889,179	12/19	89	Merenda				165		14			
I	IF	5,087,871	02/19	92	Losel				323		299			
I	ĄJ	5,089,974	02/19	92	Demeyer et al.				364		492			
	AK 5,146,401 09/1992			92	Bansal et al.					364	138			
				FC	REIGN	PATENT	DOCUMENT	rs						
		Degument No.		Date		Country			Class	Sub	Translation Yes No			
	AL	Document No. W094/01764 4021258A1		01/1994		EPO				Class	Class	s les no		
	AM				1991	Germany								
	AN				1990 United Kingdom			m						
		OTHER ART (	Includ	L		1			iner	nt Page	s, Etc.)			
F	AO Brochure: "Preliminary LTC1149, LTC1149-3.3., LTC 1149-5; High Efficiency Synchronous Stepdown Switching Regulator", Linear Technology Corp. Milpitas, CA, pp. 1-16, Nov. 1992.													
P	AΡ	Ullrich Von Seckendorff and Karen Speers, "Selecting an Inrush Current Limiter," Electronic Products, Jan. 1995, pp. 25-33.												
F	AQ "Electronics Letters an International Publication," The Institution of Electrical Engineers, August 29, 1996, vol. 32, No. 18, pp. 1629-1632.										ion of 1632.			
EXAMINE	EXAMINER:						DATE CONSIDERED:							
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Sheet 2 of 2 FORM PTO-1449 Atty. Docket No.: Appl. No.: R11.12-0856 10/825,475 First Named Inventor: LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT Garrie D. Huisenga Filing Date Group Art: April 15, 2004 2836 U.S. PATENT DOCUMENTS Examiner Document Sub Filing Date Initial No. Date Name Class Class If Appropriate 5,179,488 01/1993 AR Rovner 361 18 AS 5,187,474 02/1993 Kielb et al. 340 870 ΑT 09/1993 5,245,333 Anderson et al. 340 870 ΑU 5,258,868 11/1993 Jensen et al. 359 168 ΑV 5,333,114 07/1994 Warrior et al. 364 550 AW 5,606,513 02/1997 364 Louwagie et al. 510 AX 5,973,942 10/1999 Nelson et al. 636 21.01 ΑY FOREIGN PATENT DOCUMENTS Sub Translation Document No. Class Date Country Class Yes ΑZ OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) BB "LM193/LM293/LM393/LM2903 Low Power Low Offset Voltage Dual Comparators", National Semiconductor, pp. 1-12, January 1995. BC. "LT124/Series High Speed Current Mode Pulse Width Modulators," Linear Technology LT1241 Series, pp. 1-16, 1992. BD A.S. Koslovski, "Fast Active Inrush Current Limiter for Boost-based Resistor Emulators," 16th International Telecommunications Energy Conference, Intelec '94, Vancouver, B.C., Canada, pp. 649-652, October 30, 1994. EXAMINER: DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.